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9-bit Odd/Even Parity Generator/Checker



ADE-205-484 (Z) 1st. Edition Sep. 2000

#### Description

This parity generator/checker features odd/even outputs to facilitate operation of either odd or even parity applications. The word length capability is easily expanded by cascading devices.

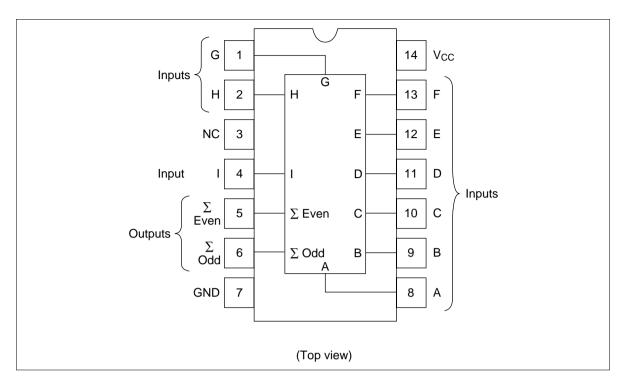
#### Features

- High Speed Operation:  $t_{pd}$  (Data to  $\Sigma$  Even or  $\Sigma$  Odd) = 22 ns typ ( $C_L$  = 50 pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage:  $V_{CC} = 2$  to 6 V
- Low Input Current: 1 µA max
- Low Quiescent Supply Current:  $I_{CC}$  (static) = 4  $\mu$ A max (Ta = 25°C)

#### **Function Table**

	Outputs				
Number of inputs A through I that are high	$\Sigma$ Even	$\Sigma$ Odd			
0, 2, 4, 6, 8	Н	L			
1, 3, 5, 7, 9	L	Н			

#### **Pin Arrangement**



#### **DC** Characteristics

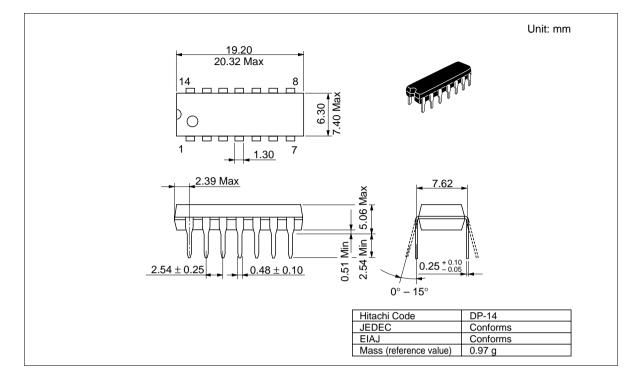
			Ta =	: 25°C		Ta = - +85°0	–40 to C			
ltem	Symbol	$V_{cc}$ (V)	Min	Тур	Max	Min	Max	Unit	Test Conditio	ns
Input voltage	V <sub>IH</sub>	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	—	—	3.15	—			
		6.0	4.2	—	—	4.2	—			
	VIL	2.0	_	_	0.5	—	0.5	V		
		4.5	—	—	1.35		1.35	_		
		6.0	_	_	1.8	—	1.8	_		
Output voltage	V <sub>OH</sub>	2.0	1.9	2.0	_	1.9	—	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OH} = -20 \ \mu A$
		4.5	4.4	4.5	_	4.4	—	_		
		6.0	5.9	6.0	_	5.9	—	_		
		4.5	4.18			4.13	—	_		I <sub>он</sub> = -4 mА
		6.0	5.68		_	5.63	—	_		I <sub>он</sub> = -5.2 mА
	V <sub>OL</sub>	2.0	_	0.0	0.1	—	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \ \mu A$
		4.5	_	0.0	0.1	_	0.1	_		
		6.0		0.0	0.1		0.1	_		
		4.5	_	_	0.26		0.33	_		$I_{OL} = 4 \text{ mA}$
		6.0	_		0.26		0.33	_		I <sub>oL</sub> = 5.2 mA
Input current	lin	6.0			±0.1		±1.0	μA	$Vin = V_{cc} \text{ or } GI$	ND
Quiescent supply current	I <sub>cc</sub>	6.0	_	—	4.0	_	40	μA	$Vin = V_{cc} \text{ or } GI$	ND, lout = $0 \mu A$

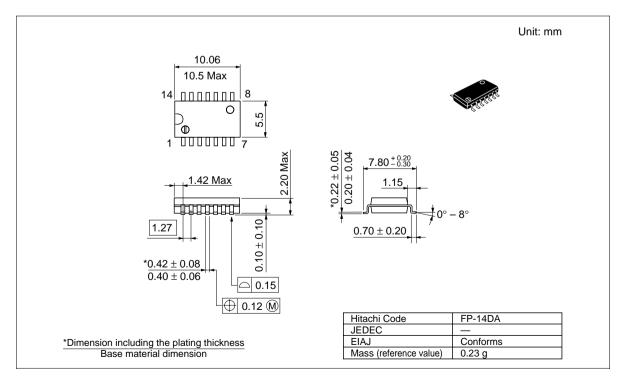
## AC Characteristics ( $C_L = 50 \text{ pF}$ , Input $t_r = t_f = 6 \text{ ns}$ )

			Ta = 25°C		Ta = –40 to +85°C				
ltem	Symbol	V <sub>cc</sub> (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t <sub>PLH</sub>	2.0	_	—	205	_	255	ns	Data to $\Sigma$ Even or $\Sigma$ Odd
time	t <sub>PHL</sub>	4.5	—	22	41	—	51		
		6.0		_	35	—	43	_	
Output rise/fall	t <sub>TLH</sub>	2.0	—	—	75	—	95	ns	
time	$t_{\text{THL}}$	4.5		5	15	—	19	_	
		6.0		_	13	—	16	_	
Input capacitance	Cin	_		5	10	—	10	pF	



#### **Package Dimensions**





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